

# Hani Jamleh (Ph.D.)

## PERSONAL INFORMATION

- 📄 **Name:** Hani Ousamah Morad Jamleh
- 📄 **Date of Birth:** February, 1981
- 📄 **Nationality:** Jordanian
- 📄 **Family:** Married with two children
- 📄 **Address:** Dahyat Al Rasheed,  
Amman 11942, Jordan
- 📄 **E-mail:** [hani.jamleh@gmail.com](mailto:hani.jamleh@gmail.com)

## WORK INTERESTS

The main theme of my work is building Electronic Design Automation (EDA) tools based on optimization techniques for the applications of VLSI physical design, design for manufacturing, microlithography simulation, nanotechnology related problems, digital image processing, and computational photography. Automatic defects detection for TFT-LCD's screen, so called MURA, is one of my concerns in the field of display technology.

One of my areas of interest include the computation of depth from captured images by real aperture camera typically using passive depth from defocus techniques.

I have been very interested in hardware design and microcontroller programming for the applications of controlling, automation, computer vision, and biomedical device integration.

## EDUCATION

- 📄 **Ph.D. in Electronics Engineering**, Graduate Institute of Electronics Engineering/Electrical Engineering Department, National Taiwan University, Taipei/Taiwan, *May/2014*.
  - **VLSI Electronic Design Automation (EDA)**
  - **Dissertation Title:** *Shallow Depth Map Estimation from Defocus Blur Point Spread Function Information.*
- 📄 **M.S. in Electrical Engineering**, Electrical Engineering Department, National Taiwan University, Taipei/Taiwan, *July/2007*.
  - **Integrated Circuit Systems (ICS)**
  - **Thesis Title:** *Hardware Accelerated Aerial Image Simulation by FPGA.*
- 📄 **B.S. in Electronics Engineering**, Hijjawi Faculty for Engineering Technology Department, Yarmouk University, Irbid/Jordan, *February/2004*.

## GRADUATE COURSES TAKEN AT NATIONAL TAIWAN UNIVERSITY (NTU)

Power Electronics	Microlithography Theory and Practice
Computer Aided Analysis & Optimization of IC	VLSI Testing
Digital Communication Integrated Circuit Design	VLSI Physical Design
Multirate Signal Processing filter Bank and Wavelet	VLSI Design Automation
Computer-Aided VLSI System Design	VLSI Design For Manufacturability
Advanced Computer Architecture	Physical Design for Nanometer ICs
Wide Gap Semiconductor Technologies	Digital Image Processing

## WORK EXPERIENCE

- 📄 **Assistant Professor**, Sep, 2015-Now  
Electrical Engineering Department, the University of Jordan, Amman/Jordan.
- 📄 **Assistant Professor**, Jun, 2015-August, 2015  
Electrical Engineering Department, American University of the Middle East, Kuwait/Kuwait.
- 📄 **Teaching Assistant, Laboratory Engineer, and students' projects supervisor** Feb, 2004-Sep, 2005  
**Subject:** 18-months, working for the department of Electronics Engineering, Yarmouk University, Irbid-Jordan.  
**Laboratories:** Taught four laboratories, Electronics Eng. I, Electronics Eng. II, Digital Logic Design, and Microprocessor (Intel 8085).

## INTERNSHIPS & TRAININGS

### ☐ **Summer Intern**, July,2010-August,2010

Two months summer internship in Taiwan Semiconductor Manufacturing Company (**TSMC**), XinChu/Taiwan.

**Subject:** Building source mask optimization (SMO) lithography simulator by Matlab along with S-Litho in order to explore the effect of the method on N65 via resolution enhancement technique (RET) from 193 to 248 technology node, and on N45 Mx RET from immersion to dry lithography. As well as, helping assist-engineers to deal with logistic engineering tasks.

**Department:** Advanced Litho-Mask Engineering Program (ALMEP)/Manufacturing Technology Center (MTC).

### ☐ **Training**, Aug. 2002 – Nov. 2002

Three months in *EETC "Egyptian Electronic Technology Company"*, Cairo/Egypt.

**Subject:** Electronic circuits design and manufacturing with SMT "Surface Mount Technology".

### ☐ **Training**, Nov. 2002 – Feb. 2003

Three months in *Imad Hanthal EST.*, Amman/Jordan.

**Subject:** PCB "Printed Circuit Board", design and manufacturing processes.

## PROFESSIONAL MEMBERSHIPS

☐ Member of *IEEE* (Institute of Electrical & Electronic Engineers), Since 2002.

☐ Member of *JEA* (Jordan Engineers Association), Since 2004.

## HONORDS & AWARDS

☐ **Scholarships** from National Taiwan University for four years in order to complete my Ph.D. studies in Electronics Engineering, (2007-2012).

☐ **Partially supported scholarship** for two-years from the Ministry of Economic Affairs and the Ministry of Education in Taiwan in order to finish my Master studies at National Taiwan University, (2005-2007).

☐ **Certificate of Appreciation for Innovation** for the project "**Home Automation: Control, Security, and Power Saving**" funded by King Abdullah II Fund for Development, Jordan Engineers Association, June 2005.

## COMPUTER SKILLS

☐ **Operating Systems:** MS-DOS, UNIX, MS-Windows.

☐ **Programming Languages:** Matlab, C, C++, Visual Basic v.6.0, Assembly (Intel 8085, and Microchip MPASM), and FORTRAN.

☐ **Electrical Eng. Programs:** Verilog, COMSOL, Synopsis tools, Mentor Graphics tools, ORCAD, and Eagle PCB layout editor.

☐ **Others:** AutoCad, and MS Office 2013.

## LANGUAGES

☐ **Arabic**, (Native Tongue).

☐ **English**, Very Good in reading, listening, speaking, and writing.

☐ **Chinese (Mandarin)**, Elementary.

## REFERENCES

☐ Upon Request.