

ANDRAWS SWIDAN

Associate Professor
Computer Engineering Department
Faculty of Engineering and Technology
University of Jordan
Tel. 5355000 ext 22990

Adjunct Professor
Electrical and Computer Engineering Department
Faculty of Engineering
McGill University
Montreal Canada

Email: sweidan@ju.edu.jo / andraws.swidan@mcgill.ca

FIELDS OF COMPETENCE

- Huge Experience in teaching, evaluation and development of programs in computer engineering.
- Organization and administrative management of programs of teaching.
- Expertise in systems security and architecture of computer and communications networks.
- Good knowledge of the ISO17799 norm and several domains of the Common Body of Knowledge of information security (CBK).
- Consultancy experience in the governmental and private sector.
- Administrative and logistic organization of seminars and scientific workshops.
- Publishing and editing scientific research.
- Certified CISSP (ID # 99128), ICDL, CCNA, A+ Software, A+ Hardware.
- Team worker and leadership spirit.

ACADEMIC EDUCATION

Electrotechnical Institute
Leningrad, Russia
1979, 1982

PhD in Computer Engineering (1982)

M.Sc. in Computer Engineering (1979)

PROFESSIONAL EXPERIENCE

McGill University
Electrical and computer
Engineering Department
Montréal
2009 - 2011
2004 - 2005

Visiting Professor

- Teaching computer engineering courses.
- Assistance in thesis supervision.
- Students' evaluation.
- Computer Engineering Research activity

University of Jordan
Electrical Engineering
Department and
Computer Engineering

Associate Professor / Assistant Professor

- Teaching Computer Engineering courses: Numerical Analysis and Optimization – *Data and communication networks* – *Engineering analysis I & II* - *Digital Design* – *Advanced Logic*

Department Amman, Jordan 1999 - now 1983 - 1999	<i>Design – Electronic devices – Digital Electronics - Computer Organization and Design – Computer Architecture – Selected topics in Computer Engineering</i> – Supervision and examination of graduate and undergraduate projects. – Preparation of academic study plans.
University of Jordan Computer Engineering Department Amman, Jordan 2005 - 2006 2001 - 2004	Chair of the Computer Engineering Department – Active participation in the establishment of the Department infrastructure. – Preparation of the graduate and undergraduate study plans. – Load distribution management. – Design and implementation of computer networks in department labs. – Recruitment and evaluation of new appointments at the department. – Active participation in tender documents preparations, evaluations of offers and approval. – Organization and supervision of administrative tasks.
Public and Private Sector Amman, Jordan 1985 – 2004	IT Consultant – Evaluation of IT needs for different public and private institutions. – Coordinator and trainer for several IT courses. – Recommendations for technological solutions and specific IT applications : ✓ <i>College de LaSalle : Introduction of IT curriculum</i> ✓ <i>Ministry of Health: Computerization and securitization of medical insurance system.</i> ✓ <i>University of Jordan : Development of online educational system</i> ✓ <i>Ministry of Interior affairs/Public security department : computerization and securitization of vehicle licensing</i> ✓ <i>Ministry of higher education and scientific research: university and programs accreditation, competency examinations.</i>

ADDITIONAL PROFESSIONAL DUTIES

- Chair and/or member of various committees : President and/or member of several university committees
- Member of the Royal IT committee.
- Member/ President of the IEEE election committee
- Member of Jordanian engineering association
- IEEE member

PUBLICATIONS PATENTS and THESES SUPERVISION:**a) International patents****11 international patents****b) List of published research papers**

1	A. Swidan, "A new approach for the hardware implementation of Volder's Algorithm for calculating the Sin and Cos functions", Proc. Int. Conf. on Computer Aided Design (CAD'84) Nice, France, June 1984.
2	A Swidan, "Error analysis of computing some Elementary Functions by Volder's algorithm", <i>Proc. Int Conf. (JIEEC 85)</i> , Amman-Jordan, April 1985, pp. 229-231
3	A Swidan, "Tabular matrix approaches for computing the Sin and Cos functions", <i>Proc. ISMM International Symposium: mini and microcomputers and their applications</i> , Spain, June 1985, pp. 515-518.
4	M. Abdel-Salam , A. Swidan, and J. Al-Zubi, "On the possibility of digital synchronization in power stations", IASTED International Conference Energy, Power and Environmental Systems Santa Barbara USA , 1985
5	A. Swidan and A. Hiasat, A new memoryless Residue to Binary converter, IEEE CAS, Nov. 1988, pp. 1441-1444.
6	A. Swidan, "Fixed-point Fractional Representation in Residue Number System", <i>the 39th midwest symposium on circuits and systems</i> , Iowa State University, USA,. August 1996.
7	A. Swidan A. Hiasat, "On the theory of error control based on moduli with common factors", <i>J. Reliable computing</i> 7, 2001, pp. 209-218.
8	A. Swidan, "A new Residue to Weighted Numbers Conversion Technique", <i>Submitted for publication in the IEEE Transactions on computers</i> , July 2001, paper 112374
9	A.Swidan and A. Hiasat, "Highly Fault tolerant RNS-based architecture", <i>Submitted for publication in the journal of system architecture</i> , January 2002.
10	Andraws Swidan, "Single-Bit-Difference Error Detection and Correction in Redundant Residue Number System", <i>Abhath Al-Yarmouk Journal;Pure Science and Engineering Series</i> , April 2003.
11	Andraws Swidan, "A New Residue to Weighted Numbers Conversion Technique", <i>Dirasat, Jordan University international refereed research journal</i> Volume 30 Engineering Sciences No 1 pages 125-134
12	A. Hiasat, Andraws Swidan "Residue Number System to Binary Converter for the moduli set $(2^{n-1}, 2^n, 2^n + 1)$ " <i>Journal of systems architecture</i> 49 (2003) 53-58
13	A. Hiasat Andraws Swidan "Residue-to-binary decoder for an enhanced moduli set " <i>IEE Proc.- Computer Digit. Tech.</i> , Vol. 151, No.2, [month] 2004.

14	Abdelfatah O., Swidan Andraws, and Zilic Z. "Direct residue -to- analog conversion scheme based on Chinese Remainder Theorem Electronics, Circuits, and Systems)ICECS), 2010 17th IEEE International Conference 12-15 December 2010. Athens Greece pages 687-691
15	Abdelfatah O., Swidan A. "Efficient direct analog-to -Residue conversion Schemes International Conference on Signals and Electronic systems (ICSES), Gliwice-Poland, September 7-10 2010. pages 85-88,

REFERENCES

Available upon request