

Curriculum Vitae

Name Mohammad Abdel-Majeed
Academic Rank Assistant Professor
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Education

PhD in Computer Engineering **2010/2016**
University of Southern California, USA.
Cumulative GPA: 3.93.
Thesis title: *"Demand Based Techniques to Improve the Energy Efficiency of the Execution Units and the Register File in General Purpose Graphics Processing Units"*
Advisor: Murali Annavaram

Master in Computer Engineering **2008/2010**
University of Southern California, USA.
Cumulative GPA: 4.0

BSc Computer Engineering **2002/2007**
University of Jordan, Jordan.
Cumulative GPA:3.85.

Professional activities in the last five years

Assistant Professor **06/2016 to Present**
University of Jordan
Instructing digital logic and digital electronics courses and labs.

Research Intern **02-08/2015**
Intel Labs, Hillsboro.
Developed a cross layer infrastructure that is used to explore new techniques for improving the memory efficiency of the java applications. During this work I worked under the supervision of Chris Wilkerson

Intern**05-08/2013****NVIDIA, Santa Clara.**

Worked on functional full-chip testing for the interactions between the CPU, GPU engines, and different memory hierarchies in Maxwell GPUs.

Invited Reviewer**01/2016-Present**

1. IEEE Transaction on Vary Large Scale Integration Systems(TVLSI)
2. Transactions on Parallel and Distributed Systems(TPDS)

Research Assistant**08/2010 to 05/2016****University of Southern California**

1. Worked on the power efficiency and reliability of graphics processing units (GPUs).
2. Mentored many undergraduate and graduate students during my PhD. This includes exposing them to the main areas in the GPU power efficiency and reliability and how to evaluate the proposed techniques using architecture simulators.

Teaching Assistant**08/2010 to 05/2016****University of Southern California**

Conducted discussion sessions and labs for Advanced computer architecture, Computer networks, Introduction to embedded systems and Introduction to programming classes.

Selected Published Research in the last five years

1. **M. Abdel-Majeed**, A.Shafaei, H.Jeon, M.Pedram, M.Annavaram, *Pilot register file: Energy Efficient Register File for GPUs*. In Proceedings of the 23rd International Symposium on High Performance Computer Architecture (HPCA), 2017
2. **M.Abdel-Majeed**, D.Wong, J.Kuang, M.Annavaram. *Origami: Folding Warps for Energy Efficient GPUs*. In Proceedings of the ACM International Conference on Supercomputing (ICS), 2016
3. **M. Abdel-Majeed**, W. Dweik, H.Jeon and M.Annavaram. *Warped-RE: LowCost Error Detection and Correction in GPUs*. In proceedings of the 45th Annual IEEE/IFIP International Conference on Dependable Systems and Networks (DSN), 2015.
4. W. Dweik, **M. Abdel-Majeed** and M.Annavaram. *Warped-Shield: Tolerating Hard Faults in GPGPUs* In proceedings of the 44th Annual IEEE/IFIP International Conference on Dependable Systems and Networks (DSN), 2014.
5. **M. Abdel-Majeed**,D. Wong and M.Annavaram. *Warped gates: gating aware scheduling and power gating for GPGPUs*. In Proceedings of the 46th Annual IEEE/ACM International Symposium on Microarchitecture, MICRO-46, 2013.
6. **M. Abdel-Majeed** and M.Annavaram. *Warped register file: A power efficient register file for GPGPUs* . In Proceedings of the 2013 IEEE 19th International

Symposium on High Performance Computer Architecture (HPCA), 2013.

7. **M. Abdel-Majeed**, Mike Chen, and M. Annavaram. *A case for 3D stacked analog circuits in high-speed sensing systems*. In Proceedings of the 2012 13th International Symposium on Quality Electronic Design (ISQED), 2012.